

Notice of References Cited	Application/Control No. 09/870,450	Applicant(s)/Patent Under Reexamination CATHERWOOD, MICHAEL I.	
	Examiner Richard Ellis	Art Unit 2183	Page 1 of 1

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	A	US-2002/0194233 A1	12-2002	KATANO, YOSHITO	708/211
	B	US-5,568,410 A	10-1996	Bechade, Roland A.	708/211
	C	US-5,349,681 A	09-1994	Yoshida et al.	712/227
	D	US-3,430,208	02-1969	AXELSON ALLEN L	708/212
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Digital VAX11/780 Architecture Handbook, 1977, Digital Equipment Corporation, pp. 3-2, and 7-16 to 7-17.
	V	Intel Pentium Processor Family Developer's Manual, Volume 3: Architecture and Programming Manual, 1995, Intel Corporation, pp. 4-12 and 25-38 to 25-39.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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